

# Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore

Shri Vaishnav Institute of Technology & Science, Indore

Choice Based Credit System (CBCS) Scheme

B Tech. (Electronics and Instrumentation)

Session 2018-2022

## V - SEMESTER

S. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/week			Examination Scheme					Total Marks	CREDITS	
				L	T	P	Theory		Practical		End Sem University Exam			Teachers Assessment*
							End Sem University Exam	Two Term Exam	End Sem University Exam	Teachers Assessment*				
1	BTEIS04	Digital Signal Processing		3	1	2	60	20	20	30	20	150	5	
2	BTEIS01	Control System Engineering		3	1	2	60	20	20	30	20	150	5	
3	BTECS04	CMOS VLSI Design		3	1	2	60	20	20	30	20	150	5	
4	BTEIS02	Fiber Optic Instrumentation		3	1	0	60	20	20	0	0	100	4	
5	BTECS03 BTECS15 BTEIS01 BTECS11	Elective		3	1	2	60	20	20	30	20	150	5	
6	BTEIS03	Labview		0	0	2	0	0	0	30	20	50	1	
7	BTEIS04	Minor Project		0	0	2	0	0	0	30	20	50	1	
<b>TOTAL</b>				<b>15</b>	<b>5</b>	<b>12</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>180</b>	<b>120</b>	<b>800</b>	<b>26</b>	

\*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Elective	Data Structure & Algorithms (BTECS 405)	3	1	2	60	20	20	30	20	20	150	5
	Data Communication (BTECS15)	3	1	2	60	20	20	30	20	20	150	5
	Automation (BTEIS11)	3	1	2	60	20	20	30	20	20	150	5
	Instrumentation system design (BTEIS11)	3	1	2	60	20	20	30	20	20	150	5

*(Signature)*

**Chairperson**  
**Board of Studies**

Shri Vaishnav Vidyapeeth Vishwavidyalaya  
Indore

*(Signature)*  
**Registrar**  
Shri Vaishnav Vidyapeeth Vishwavidyalaya